

μPD78F9177CU

Product Description

The μPD78F9177 is μPD789166 Subseries (small, general-purpose) in the 78K/0S Series.

The μPD78F9177 replaces the internal ROM of the μPD789166 with flash memory. Because flash memory allows the program to be written and erased electrically with the device mounted on the board, this product is ideal for the evolution stages of system development, small-scale production and rapid development of new products.

Detailed function descriptions are provided in the following user's manuals. Be sure to read them before designing.

μPD789167, 789177, 789167Y, 789177Y Subseries User's Manual: U14186E
78K/0S Series User's Manual Instruction: U11047E

FEATURES

- * Pin compatible with mask ROM version (except VPP pin)
- * Flash memory: 24 Kbytes
- * High-speed RAM: 512 bytes
- * Minimum instruction execution time can be changed from high-speed (0.4 μs: @5.0-MHz operation with main system clock) to ultra-low-speed (122 μs: @ 32.768-kHz operation with subsystem clock)
- * 10-bit resolution A/D converter: 6 channels
- * I/O ports: 23
- * Serial interface: 1 channel
- * 3-wire serial I/O mode / UART mode: 1 channel
- * Timers: 6 channels
- * 16-bit timer: 1 channel
- * 8-bit timer/event counter: 2 channels
- * 8-bit timer: 1 channel
- * Watch timer: 1 channel
- * Watchdog timer: 1 channel
- * On-chip 16-bit multiplier
- * Power supply voltage: VDD = 4.5 to 5.5 V

APPLICATIONS

Air-conditioner, White goods, etc

ORDERING INFORMATION
 μPD78F9177CU

Part Number

Package

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42-pin plastic SDIP(600 mil)

Product Image

Vss0	1	42	X1
Vdd0	2	41	X2
T180/SS20/P25	3	40	RESET
TO80/P26	4	39	XT1
P00	5	38	XT2
P01	6	37	Vpp
P02	7	36	P24
P03	8	35	P23
P04	9	34	P22/SI20/RxD20
Vss1	10	33	P21/SO20/TxD20
P05	11	32	Vdd1
P50	12	31	P20/SCK20/ASCK20
P51	13	30	P33/TO82/INTP3/BZO90
P52	14	29	P32
P53	15	28	P31/INTP1/TO81
Avvd	16	27	P30/CPT90/INTP0/TI81
Avref	17	26	P11
ANI0	18	25	P10
ANI1	19	24	Avss
ANI2	20	23	ANI5
ANI3	21	22	ANI4