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[TMR1303S](#)

EN

This Datasheet is presented by
the manufacturer

DE

Dieses Datenblatt wird vom
Hersteller bereitgestellt

FR

Cette fiche technique est
présentée par le fabricant

General Description

TMR1303 is a digital omnipolar magnetic switch that integrates TMR and CMOS technology in order to provide a magnetically triggered digital switch with high sensitivity, high speed, and ultra-low power consumption. It is designed for use in applications that are both power-critical and performance-demanding. It contains a push-pull half-bridge TMR magnetic sensor and CMOS signal processing circuitry within the same package, including an on-chip TMR voltage generator for precise magnetic sensing, a TMR voltage amplifier and comparator plus a Schmitt trigger to provide switching hysteresis for noise rejection, and CMOS push-pull output. An internal band gap regulator is used to provide a temperature compensated supply voltage for internal circuits, permitting a wide range of supply voltages. The TMR1303 draws only 1.5 μ A resulting in ultra-low power operation. It has fast response, accurate switching points, excellent thermal stability, and immunity to stray field interference. It is available in the SOT23-3, TO92S and LGA3L 2x1.5x0.63 package.

Features and Benefits

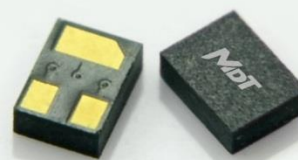
- Tunneling Magnetoresistance (TMR) Technology
- Ultra Low Power Consumption at 1.5 μ A
- High Frequency Response > 1kHz
- Operation with North or South Pole
- Low Switching Points for High Sensitivity
- Compatible with a Wide Range of Supply Voltages
- Excellent Thermal Stability
- High Tolerance to External Magnetic Field Interference

Applications

- Utility Meters including Water, Gas, and Heat Meters
- Proximity Switches
- Position and Speed Sensing
- Motor and Fan Control

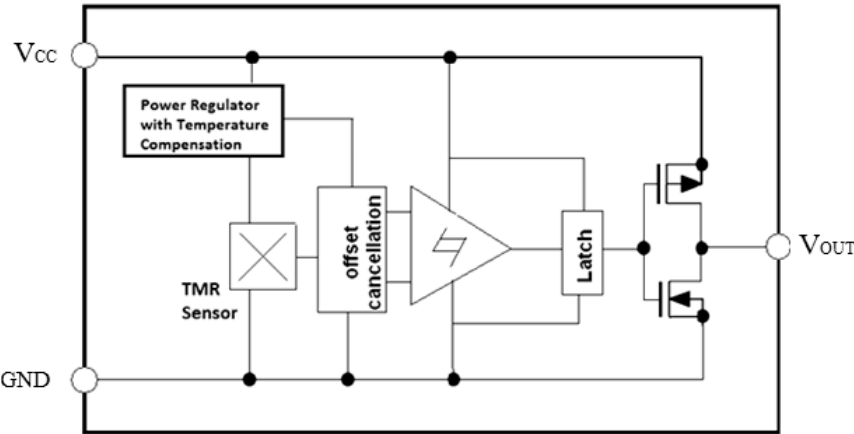


TMR1303S(Left), TMR1303T(Right)



TMR1303G

Block Diagram



Pin Configuration

SOT23-3

TO-92S

Pin Name	Pin No.			Pin Function
	TO-92S	SOT23-3	LGA3L	
V _{OUT}	1	2	2	Output
GND	2	3	1	Ground
V _{CC}	3	1	3	Supply Voltage

LGA3L 2x1.5x0.63

Absolute Maximum Ratings

Parameter	Symbol	Limit	Unit
Supply Voltage	V _{CC}	7	V
Reverse Supply Voltage	V _{RCC}	0.3	V
Output Current	I _{OUTSINK}	9	mA
Magnetic Flux Density	B	2800	G
ESD level(HBM)	V _{ESD}	4	kV
Operating Ambient Temperature	T _A	-40 ~125	°C
Storage Temperature	T _{stg}	-50 ~ 150	°C

Electrical Characteristics (V_{CC}=3.0V, T_A=25°C)

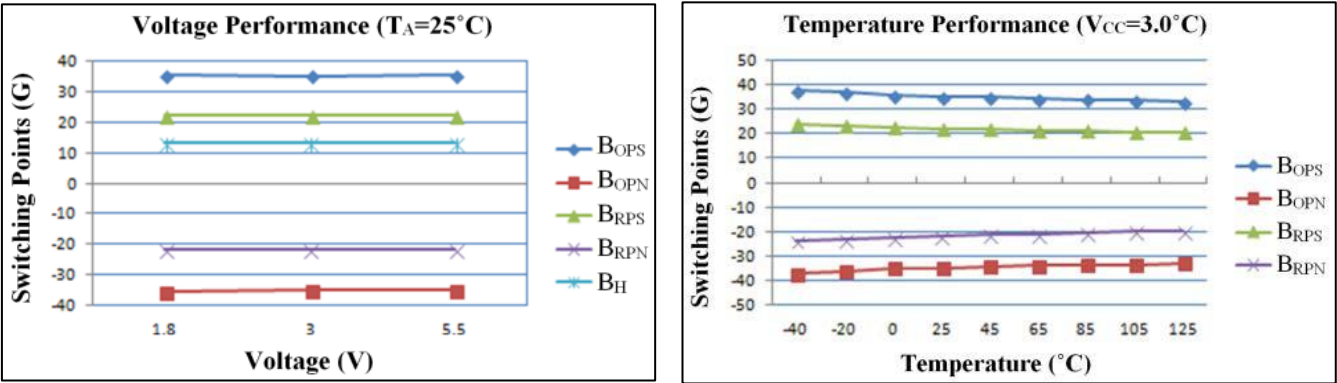
Parameter	Symbol	Conditions	Min	Typ.	Max	Unit
Supply Voltage	V _{CC}	Operating	1.8	3.0	5.5	V
Output High Voltage	V _{OH}		V _{CC} -0.3		V _{CC}	V
Output Low Voltage	V _{OL}		0		0.2	V
Supply Current	I _{CC}	Output Open		1.5		μA
Response Frequency	F			1000		Hz

Note: a 100nF capacitor is connected between V_{CC} and GND during all tests in the above table.

Magnetic Characteristics (V_{CC} = 3.0V, T_A = 25°C)

Parameters	Symbol	Min	Typ.	Max	Units
Operate Point	B _{OPS}		35		G
	B _{OPN}		-35		G
Release Point	B _{RPS}		22		G
	B _{RPN}		-22		G
Hysteresis	B _H		13		G

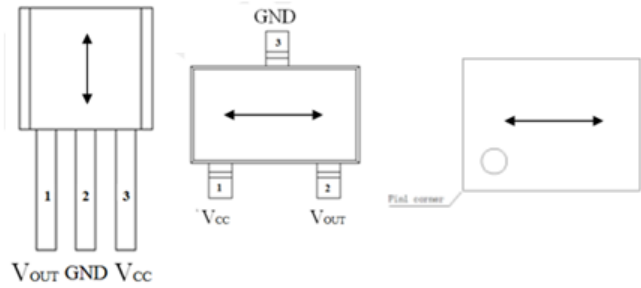
Voltage and Temperature Characteristics



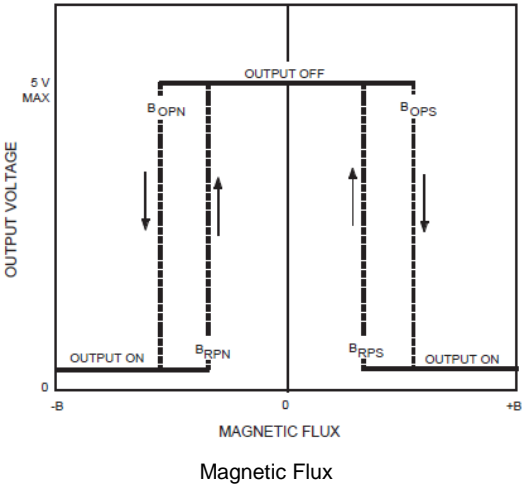
Output Behavior vs. Magnetic Pole

Parameter	Test Conditions	Output
South Pole	B > B _{OPS}	Low (On)
	0 < B < B _{RPS}	High (Off)
North Pole	B < B _{OPN}	Low (On)
	0 > B > B _{RPN}	High (Off)

Note: when power is turned on under zero magnetic field, the output is “High”.



Sensing Direction of Magnetic Field

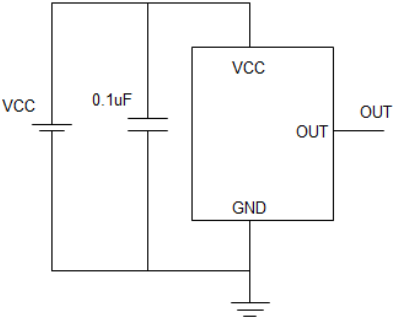


Magnetic Flux

Application Information

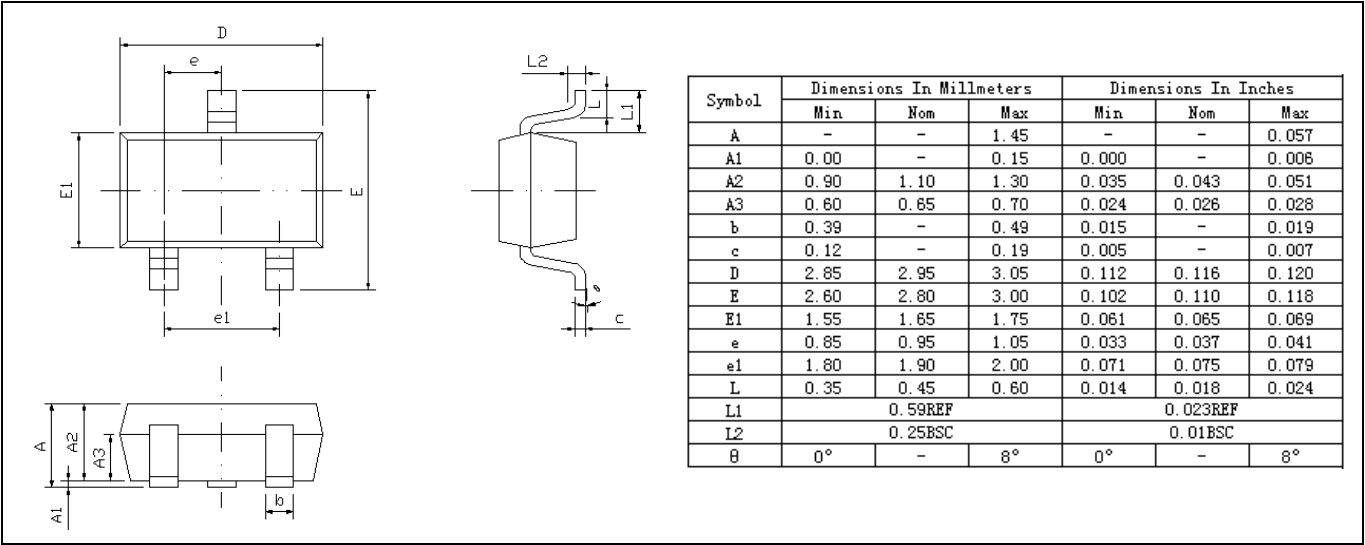
The output of the TMR1303 switches low (turns on) when a magnetic field to the sensing axis exceeds the operate point threshold, B_{OP} . When the magnetic field is reduced below the release point, B_{RP} , the device output switches high (turns off). The difference between the B_{OP} and B_{RP} is the hysteresis B_H of the device.

It is strongly recommended that an external bypass capacitor be connected in close proximity to the device between the supply and ground pins to reduce noise. The recommended value for the external bypass capacitor is 0.1 μ F.

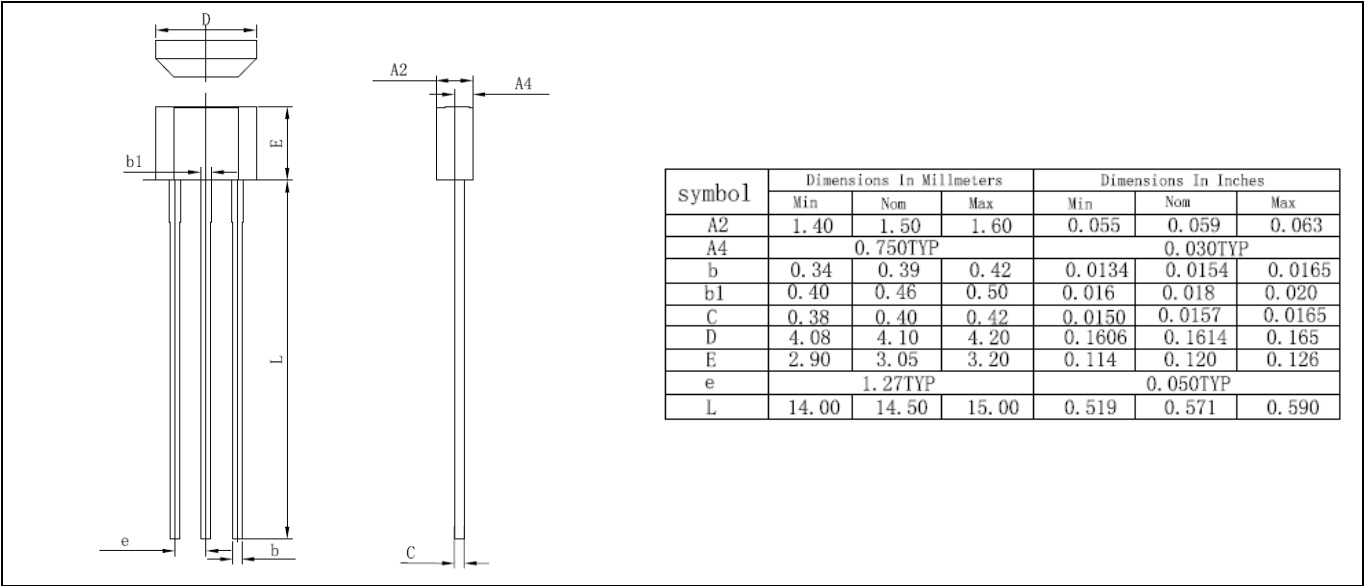


Package Information

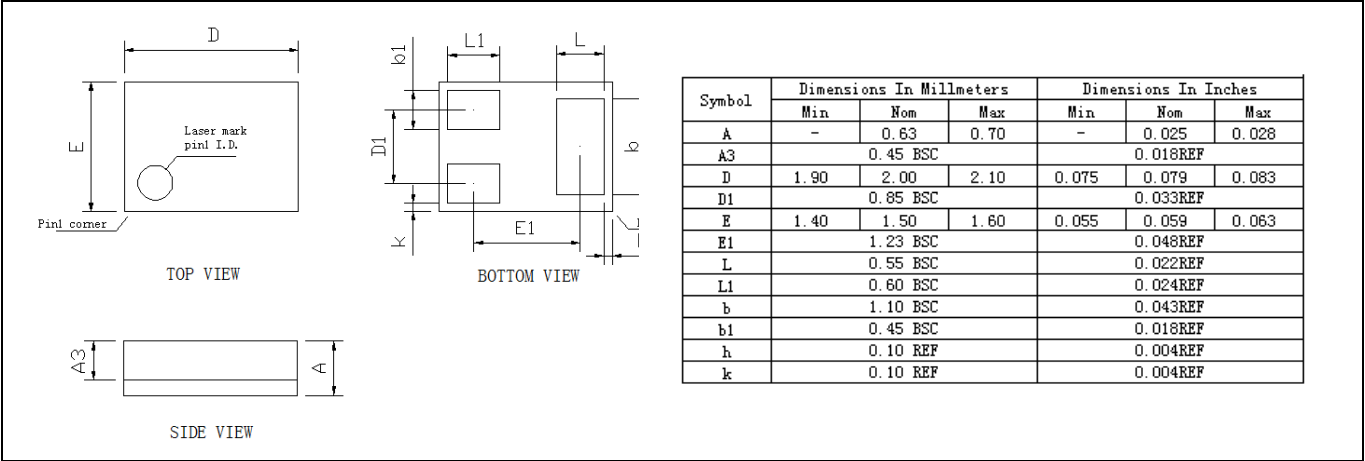
SOT23-3 package drawing



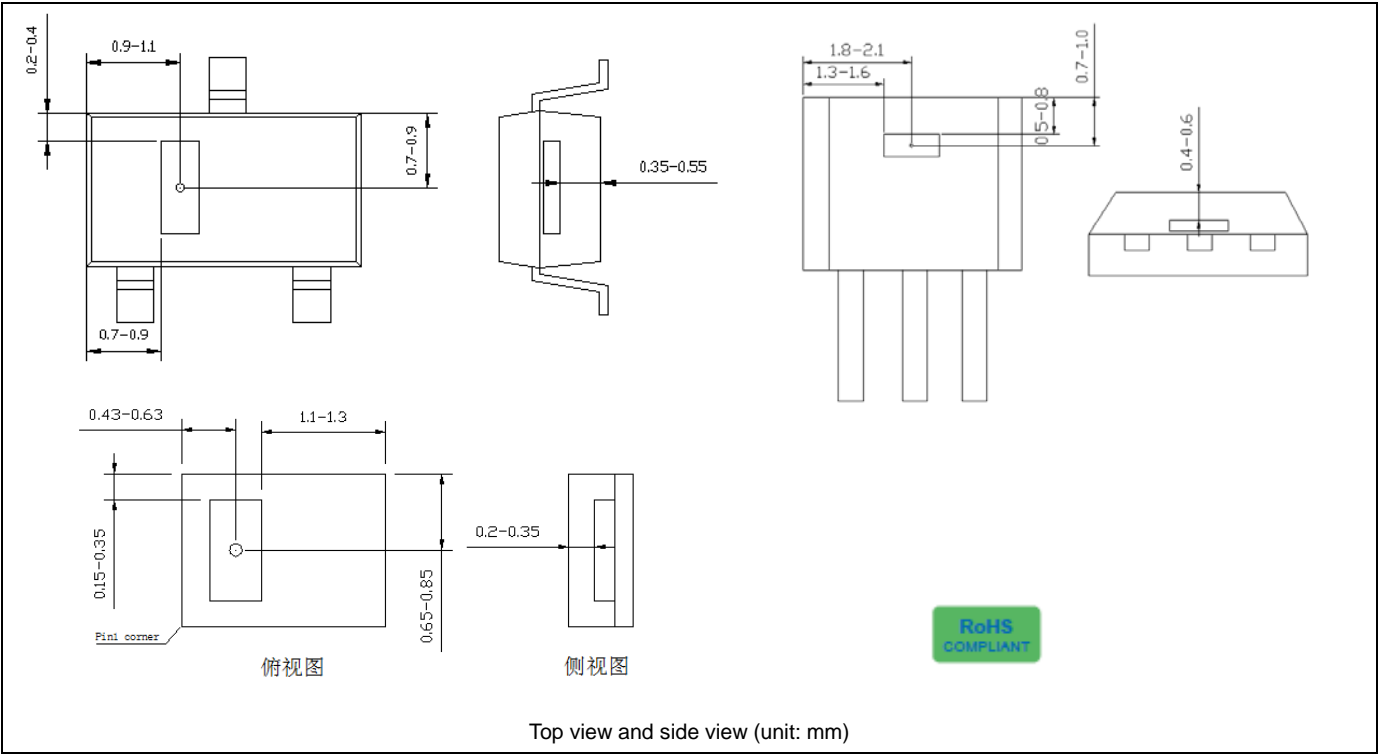
TO-92S package drawing



LGA3L package drawing



TMR Sensor Position





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