TOSHIBA Insulated Gate Bipolar Transistor Silicon N Channel IGBT

GT8G134

Strobe Flash Applications

- Compact and Thin (TSSOP-8) package
- Enhancement-mode
- Peak collector current: Ic = 150 A (max)

(@V_{GE}=2.5V(min))/

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-emitter voltage		V _{CES}	400	V	
Gate-emitter voltage	DC	V_{GES}	± 4	X	
	Pulse	V_{GES}	± 5		
Collector current	Pulse (Note 1)	I _{CP}	150		
Collector power dissipation (t=10 s)	(Note 2a)	P _C (1)	1.1	W	
	(Note 2b)	P _C (2)	0.6	W	
Junction temperature		Tj	150	√ °C	
Storage temperature range		T _{stg}	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept) and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

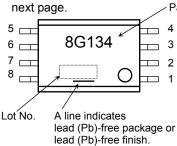
Thermal Characteristics

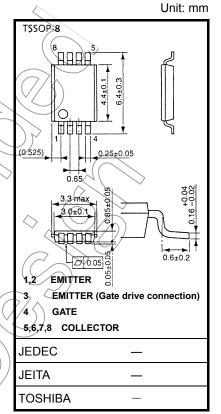
Characteristics	Symbol Rating		Unit	
Thermal resistance, junction to ambient (t = 10 s) (Note2a)	R _{th} (j-a) (1)	114	°C/W	
Thermal resistance, junction to ambient (t = 10 s) (Note2b)	R _{th (j-a)} (2)	208	°C/W	

Marking (Note 3)

Note: For (Note 1), (Note 2a), (Note 2b) and (Note 3), Please refer to the next page.

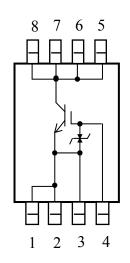
Part No. (or abbreviation code)





Weight: 0.035 g (typ.)

Circuit Configuration

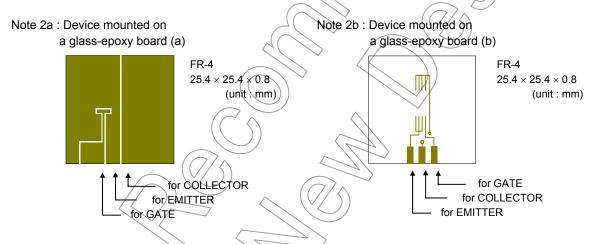


Electrical Characteristics (Ta = 25°C)

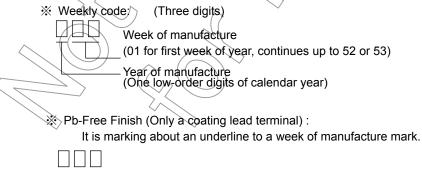
Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curi	rent	I _{GES}	$V_{GE} = \pm 4 \text{ V}, V_{CE} = 0$	_	_	± 10	μА
Collector cut-off c	urrent	I _{CES}	V _{CE} = 400 V, V _{GE} = 0	_	_	10	μΑ
Gate-emitter cut-o	off voltage	V _{GE} (OFF)	I _C = 1 mA, V _{CE} = 5 V	0.65	1.0	1.35	V
Collector-emitter	saturation voltage	V _{CE (sat)}	I _C = 150 A, V _{GE} = 2.5 V		3.4	_	V
Input capacitance		C _{ies}	$V_{CE} = 10 \text{ V}, V_{GE} = 0, f = 1 \text{ MHz}$		¥560	_	pF
Switching time	Rise time	t _r	$\begin{array}{c} 3 \ \lor \\ 0 \\ \hline \\ V_{\text{IN}}: \ t_r \leq 100 \ \text{ns} \\ t_f \leq 100 \ \text{ns} \\ \hline \\ \text{Duty cycle} \leq 1\% \\ \end{array}$) \{\frac{1}{2}}	0.6	_	μs
	Turn-on time	t _{on}			0.8	_	
	Fall time	t _f			1.2	_	
	Turn-off time	t _{off}		_ (1.8	\searrow	

Note

Note 1: Please use devices on condition that the junction temperature is below 150°C. Repetitive rating: pulse width limited by maximum junction temperature.



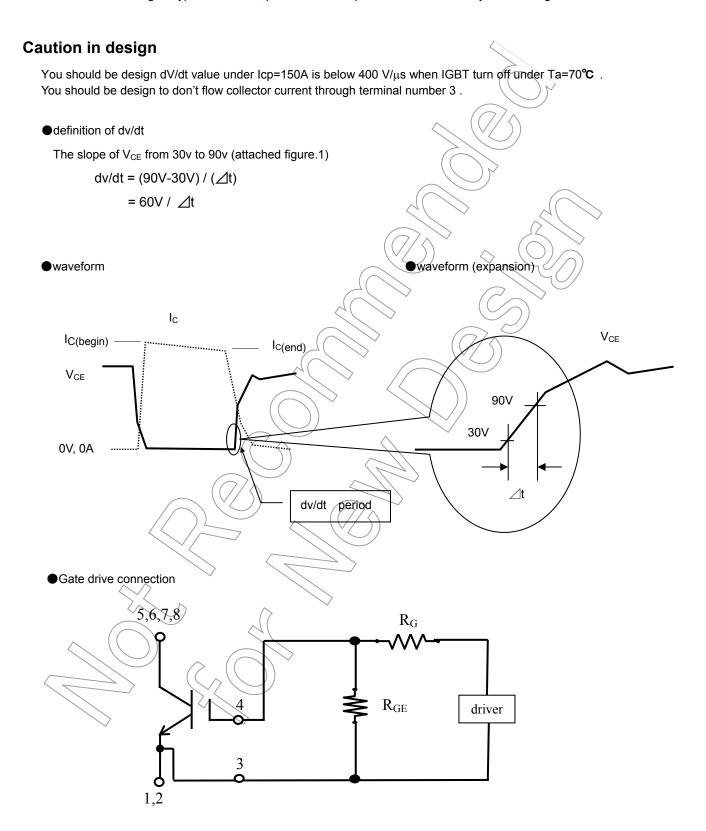
Note 3: O on lower right of the marking indicates Pin 1.

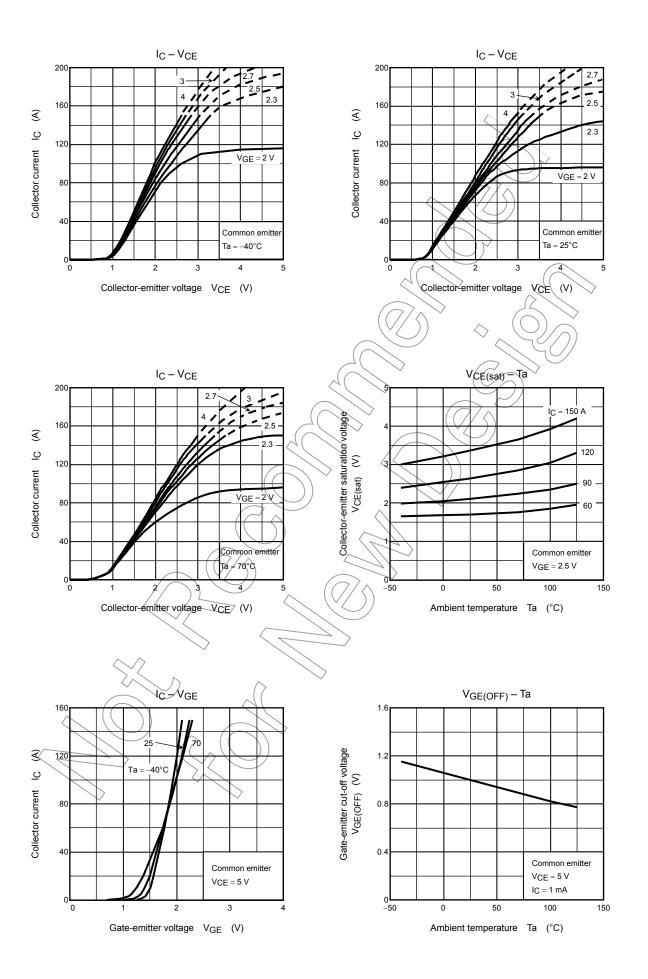


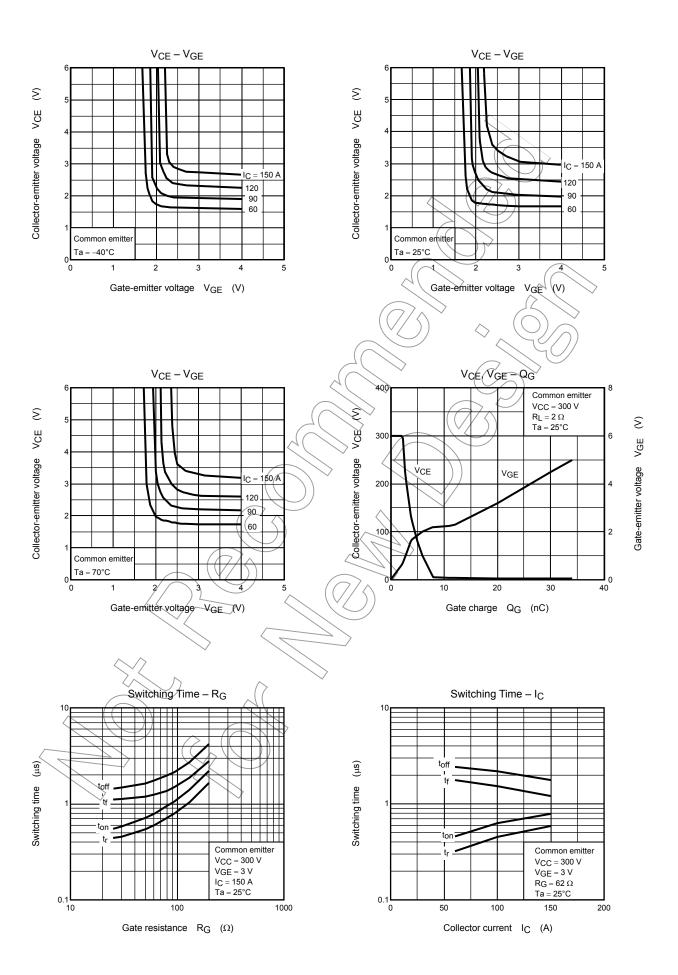
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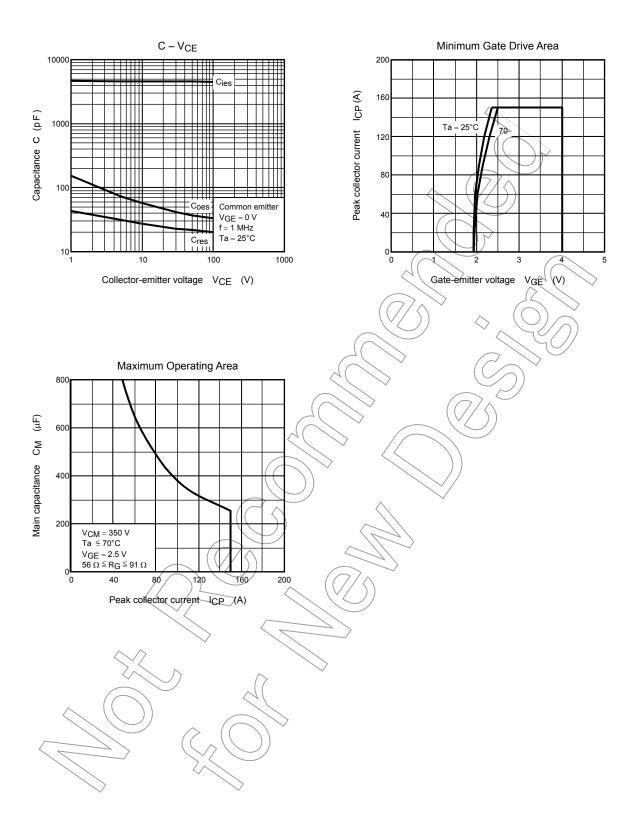
Caution on handling

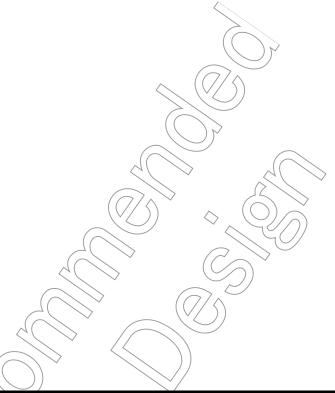
This device is MOS gate type. Therefore, please care of a protection from ESD in your handling.











RESTRICTIONS ON PRODUCT USE

20070701-EN

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 In developing your design places appure that TOSHIBA products are used within appointed energing representation.
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