

# Actel

Actel Corporation



<b>ISIN</b>	<a href="#">US0049341052</a>
<b>Industry</b>	<a href="#">Integrated Circuits</a>
<b>Successor</b>	<a href="#">Microsemi</a>
<b>Founded</b>	1985
<b>Headquarters</b>	<a href="#">San Jose, California</a> , United States
<b>Products</b>	<a href="#">FPGAs</a> , Embedded Processors
<b>Revenue</b>	▲ <a href="#">US\$191 Million (FY 2009)</a> <sup>[1]</sup>
<b>Operating income</b>	▼ <a href="#">US\$-21.3 Million (FY 2009)</a> <sup>[1]</sup>
<b>Net income</b>	▼ <a href="#">US\$-46.2 Million (FY 2009)</a> <sup>[1]</sup>
<b>Total assets</b>	▼ <a href="#">US\$307 Million (FY 2009)</a> <sup>[2]</sup>
<b>Total equity</b>	▼ <a href="#">US\$233 Million (FY 2009)</a> <sup>[2]</sup>
<b>Number of employees</b>	500+ <sup>[3]</sup>
<b>Parent</b>	<a href="#">Microsemi</a>
<b>Website</b>	<a href="#">www.actel.com</a> <a href="#">www.microsemi.com</a>

**Actel Corporation** (formerly [NASDAQ:ACTL](#)) (now [Microsemi](#)) was an American manufacturer of nonvolatile, low-power field-programmable gate arrays ([FPGAs](#)),<sup>[4]</sup> mixed-signal FPGAs,<sup>[5]</sup> and programmable logic solutions.<sup>[6][7][8]</sup> It was headquartered in [Mountain View, California](#), with offices worldwide.

Actel Corporation has been acquired by [Microsemi](#).



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## History and competition

Actel was founded in 1985 and became known for its high-reliability and [antifuse](#)-based FPGAs, used in the military and aerospace markets.<sup>[9]</sup>

In 2000, Actel acquired GateField which expanded Actel's antifuse FPGA offering to include flash-based FPGAs. In 2004, Actel announced it had shipped the one-millionth unit of its flash-based ProASIC<sup>PLUS</sup> FPGA.<sup>[10]</sup>

In 2005, Actel introduced a new technology known as Fusion to bring FPGA programmability to mixed-signal solutions. Fusion was the first technology to integrate mixed-signal analog capabilities with flash memory and FPGA fabric in a monolithic device.<sup>[11]</sup>

In 2006, to address the tight power budgets of the portable market, Actel introduced the IGLOO FPGA. The IGLOO family of FPGAs was based on Actel's nonvolatile flash technology and the ProASIC 3 FPGA architecture.<sup>[12]</sup> Two new IGLOO derivatives were added in 2008: IGLOO PLUS FPGAs with enhanced I/O capabilities, and IGLOO nano FPGAs, a low power solution at 2  $\mu$ W. A nano version of ProASIC3 also became available in 2008.

In 2010, Actel introduced the [SmartFusion](#) line of FPGAs. SmartFusion includes both analog components and a programmable flash-based logic fabric within the same chip. SmartFusion was the first FPGA product to additionally include a hard ARM processor core.<sup>[13]</sup>

[Altera](#) and [Xilinx](#) are the other key players in the market, however their main focus is on SRAM FPGAs. [Lattice Semiconductor](#) is another competitor.<sup>[14][15]</sup>

In November 2010, Actel Corporation was acquired by Microsemi.<sup>[16][17][18]</sup>

## Technologies

Actel's portfolio of FPGAs is based on two types of technologies: antifuse-based FPGAs (Axcelerator, SX-A, eX, and MX families) and flash-based FPGAs (Fusion, PolarFire, IGLOO, and ProASIC3 families).

Actel's antifuse FPGAs have been known for their nonvolatility, live at power-up operation<sup>[citation needed]</sup>, single-chip form factor<sup>[clarification needed][citation needed]</sup>, and security<sup>[citation needed]</sup>. Actel's flash-based FPGA families include these same characteristics<sup>[citation needed]</sup> and are also reprogrammable and low power.<sup>[citation needed]</sup>

Actel also develops system-critical FPGAs (RTAX and ProASIC3 families), including extended temperature automotive, military, and aerospace FPGAs, plus a wide variety of space-class

radiation-tolerant devices. These flash and antifuse FPGAs have high levels of reliability<sup>[[citation needed](#)]</sup> and firm-error immunity<sup>[[clarification needed](#)][[citation needed](#)]</sup>.

## Controversy

In March 2012, researchers from Cambridge University discovered that a backdoor exists in the JTAG interface of the ProASIC3 family of low-powered FPGAs.<sup>[19]</sup> They defended their theory at a [cryptography](#) workshop held in Belgium in September 2012.<sup>[20]</sup>