# PQ30RV1/PQ30RV11/PQ30RV2/PQ30RV21

Variable Output Low Power-Loss Voltage Regulators

#### Features

- Compact resin full-mold package
- Low power-loss (Dropout voltage: MAX.0.5V)
- Variable output voltage (setting range: 1.5 to 30V)
- Built-in output ON/OFF control function

#### Applications

- Power supply for print concentration control of electronic typewriters with display
- Series power supply for motor drives
- Series power supply for VCRs and TVs

#### Model Line-ups

•				
Output voltage	1A output	2A output		
Reference voltage precision:±4%	PQ30RV1	PQ30RV2		
Reference voltage precision:±2%	PQ30RV11	PQ30RV21		

#### **Outline Dimensions** (Unit:mm) 4.5±0.2 10.2MAX 2.8±0.2 $3.6\pm0.2$ $7.4\pm0.2$ ø3.2±0.1 4.8MAX PO30RV31 $5.6\pm0.5$ 29.1MAX SHARP 4-1.4 3.5MIN 4-0 6 +0.3 (1.5)(0.5) 3 - (2.54)Internal connection diagram DC input(VIN) 1 2 DC output(Vo) 2 3 GND ă Output voltage Specific IC minute 4 adjustment terminal(VADJ)

#### Equivalent Circuit Diagram



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#### PQ30RV1/PQ30RV11/PQ30RV2/PQ30RV21

#### Absolute Maximum Ratings

Parameter		Symbol	Rating	Unit	
*1 Input voltage		VIN	35	V	
*1 Output voltage adjustment voltage		VADJ	7	V	
Output current	PQ30RV1/PQ30RV11	Io	1	٨	
	PQ30RV2/PQ30RV21	10	2	— A	
Power dissipation (No heat sink)		P <sub>D1</sub>	1.5	W	
Power dissipation	PQ30RV1/PQ30RV11	PD2	15	— w	
(With infinite heat sink)	PQ30RV2/PQ30RV21	1 D2	18		
*2 Junction temperature		Tj	150	°C	
Operating temperature		Topr	-20 to +80	°C	
Storage temperature		Tstg	-40 to +150	°C	
Soldering temperature		Tsol	260(For 10s)	°C	

 $^{\ast1}$  All are open except GND and applicable terminals.

\*2 Overheat protection may operate at Tj>=125°C.

#### **Electrical Characteristics**

Unless otherwise specified, condition shall be  $V_{IN=15V}$ ,  $V_{O=10V}$ ,  $I_{O=0.5A}$ ,  $R_{I=390\Omega}(PQ30RV1/PQ30RV11)$  $V_{IN=15V}$ ,  $V_{O=10V}$ ,  $I_{O=1.0A}$ ,  $R_{I=390\Omega}(PQ30RV2/PQ30RV21)$ 

(Ta=25°C)

 $(T_a=25^{\circ}C)$ 

					(14 10 0)			
Pa	rameter	Symbol	Conditions		MIN.	TYP.	MAX.	Unit
Input voltage		VIN	-		4.5	-	35	V
Output voltage	PQ30RV1/PQ30RV2	Vo	R <sub>2</sub> =94Ω to 8.5k	2 15		-	30	v
	PQ30RV11/PQ30RV21	vo	R <sub>2</sub> =84Ω to 8.7kΩ		1.5			
Load regulation	PQ30RV1/PQ30RV11	рт	Io=5mA to 1A		- 0.3 1.0		%	
	PQ30RV2/PQ30RV21	RegL	Io=5mA to 2A		-	0.5	1.0	70
Line regulation		RegI	VIN=11 to 28V		-	0.5	2.5	%
Ripple rejection		RR	Cref=0	Defente Fig. 2	45	55	-	dB
Ripple rejection		KK	Cref=3.3µF	Refer to Fig. 2	55	65	-	ub
Reference voltage	PQ30RV1/PQ30RV2	Vref			1.20	1.25	1.30	v
	PQ30RV11/PQ30RV21		_		1.225	1.25	1.275	v
Temperature coeffi	cient of reference voltage	TcVref	Tj=0 to 125°C		-	±1.0	-	%
Dropout voltage	PQ30RV1/PQ30RV11	Vi-0	**3, Io=0.5A			-	0.5	v
	PQ30RV2/PQ30RV21		**3, Io=2A		_			
Quiescent current		Iq	Io=0		-	_	7	mA

\*3 Input voltage shall be the value when output voltage is 95% in comparison with the initial value.

#### Fig. 1 Test Circuit







 $V_0=V_{ref} \times \left(1+\frac{R_2}{R_1}\right)$ 

[R1=390Ω,Vref Nearly=1.25V]

Io=0.5A f=120Hz(sine wave) ei(rms)=0.5V RR=20 log(ei(rms)/eo(rms))

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#### Low Power-Loss Voltage Regulators

#### Fig. 3 Power Dissipation vs. Ambient Temperature (PQ30RV1/PQ30RV11)



Note) Oblique line portion : Overheat protection may operate in this area.



## Fig. 5 Overcurrent Protection Characteristics

### PQ30RV1/PQ30RV11/PQ30RV2/PQ30RV21









R1=390Ω,R2=2.7kΩ,VIN=15V

50

Junction temperature T<sub>j</sub> (°C)

Io=

25

0

Io=0.5A(PQ30RV1/PQ30RV11) 1A(PQ30RV2/PQ30RV21)

75

100

125

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0

-10

-25











Fig.10 Output Voltage vs. Input Voltage (PQ30RV2/PQ30RV21)



#### Fig.12 Dropout Voltage vs. Junction Temperature (PQ30RV2/PQ30RV21)



Fig.14 Ripple Rejection vs. Input Ripple Frequency (PQ30RV1/PQ30RV11)



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Fig.18 Output Peak Current vs. Dropout Voltage (PQ30RV1/PQ30RV11)



Fig.20 Output Peak Current vs. Junction Temperature (PQ30RV1/PQ30RV11)







#### **Standard Connection**



- $D_1$ : This device is necessary to protect the element from damage when reverse voltage may be applied to the regulator in case of input short-circuiting.
- Cref : This device is necessary when it is required to enhance the ripple rejection or to delay the output start-up time(\*\*1).
  (\*1)Otherwise, it is not necessary.
  (Care must be taken since Cref may raise the gain, facilitating oscillation.)

(\*1)The output start-up time is proportional to Cref $\times$ R<sub>2</sub>.

- $C_{IN}$ ,  $C_O$ : Be sure to mount the devices  $C_{IN}$  and  $C_O$  as close to the device terminal as possible so as to prevent oscillation. The standard specification of  $C_{IN}$  and  $C_O$  is  $0.33\mu$ F and  $47\mu$ F, respectively. However, ajust them as necessary after checking.
- $\begin{array}{ll} R_1, R_2 &: \mbox{These devices are necessary to set the output voltage. The output voltage Vo is given by the following formula:} & Vo=Vref X (1+R_2/R_1) & (Vref is 1.25V TYP) \end{array}$

The standard value of  $R_1$  is 390  $\Omega$  . But value up  $10k\Omega$  does not cause any trouble.

#### **ON/OFF** Operation



- ON/OFF operation is available by mounting externally D<sub>2</sub> and R<sub>3</sub>.
- When V<sub>ADJ</sub> is forcibly raised above V<sub>ref</sub>(1.25V TYP) by applying the external signal, the output is turned off(pass transistor of regulator is turned off). When the output is OFF, V<sub>ADJ</sub> must be higher then V<sub>ref</sub> MAX., and at the same time must be lower than maximum rating 7V.

In OFF-state, the load current flows to RL from VADJ through R2. Therefore the value of R2 must be as high as possible.

• Vo'=VADJ $\times$ RL/(RL+R2)

occurs at the load. OFF-state equivalent circuit  $R_1$  up to  $10k\Omega$  is allowed. Select as high value of  $R_L$  and  $R_2$  as possible in this range. In some case, as output voltage is getting lower (Vo<1V), impedance of load resistance rises. In such condition, it is sometime impossible to obtain the minimum value of Vo'. So add the dummy resistance indicated by  $R_D$  in the figure to the circuit parallel to the load.

#### An Example of ON/OFF Circuit Using the 1-chip Microcomputer Output Port(PQ30RV1)

<Specification> Output port of microcomputer VoH(max)=0.5 V VoH(min)=2.4 V (IoH=0.2mA) MAX. rating of IoH=0.5mA Output should be set as follows. 15.6V RL=52Ω(Io=0.3A)

From  $V_0=1.25V(1+R_2/R_1)$  we get  $V_0=15.6V$ .

 $R_2/R_1=11.48$ 

Assuming that  $V_F(max)=0.8V$  for D<sub>2</sub> in case of  $V_{OH}(min)=2.4V$ , we get  $V_{ADJ}=V_{OH}(min)-V_F(max)=2.4V-0.8V=1.6V$ . From  $V_{ref}(max)=1.3V$  we get  $R_3=0\Omega$ 

If  $R_1=10k\Omega$ , we get  $R_2=11.48 \times R_1=114.8 k\Omega$  and IoH as follows, ingnoring  $R_L$  (52 $\Omega$ ):

 $I_{OH}=1.6V \times (R_1+R_2)/R_1 \times R_2$ 

 $=1.6V \times (10 k \Omega + 114.8 k \Omega) / 10 k \Omega \times 114.8 k \Omega = 0.17 m A$ 

Hence, IoH<0.2mA. Therefore Voh(min) is ensured.

Next, assuming that  $V_F(min)=0.5V$  for  $D_2$  in case of  $V_{OH}(max)$ , we get:

 ${\rm Io}{\rm H}{=}\left(5V{-}0.5V\right)(R_1{+}R_2)/R_1{\times}R_2{=}0.49mA$  which is less than the rating.

 $Figure 1 shows the Vo-Vc characteristics when R_1=10k\Omega, R_2=115k\Omega, R_3=0\Omega, V_{1N}=17V, R_1=52\Omega, and D_1=1S2076A (Hitachi).$ 

Output Voltage vs. Control Voltage(PQ30RV1)



#### Model Line-ups for Lead Forming Type

Output current	1A output	2A output
Output voltage precision:±2.5%	PQ30RV1B	PQ30RV2B

#### Outline Dimensions(PQ30RV1B/PQ30RV2B)

(Unit:mm)



Note) The value of absolute maximum ratings and electrical characteristics is same as ones of PQ30RV1/2 series.

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