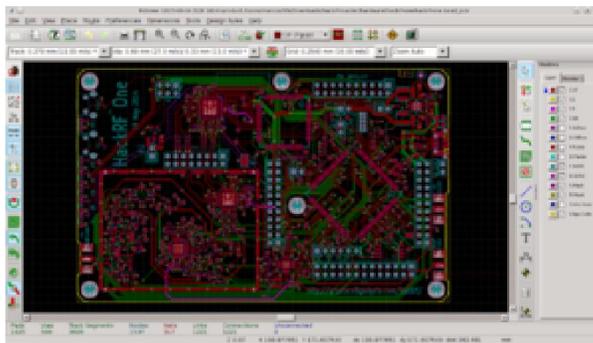


# KiCad

KiCad



KiCad Layout Editor

**Original author(s)** Jean-Pierre Charras  
**Developer(s)** KiCad developers<sup>[1]</sup>  
**Initial release** 1992; 28 years ago<sup>[2]</sup>  
**Stable release** 5.1.5<sup>[3]</sup> / 28 November 2019; 2 months ago

- [gitlab.com/kicad/code/kicad](https://gitlab.com/kicad/code/kicad)

## Repository

**Written in** [C++](#)<sup>[4]</sup>

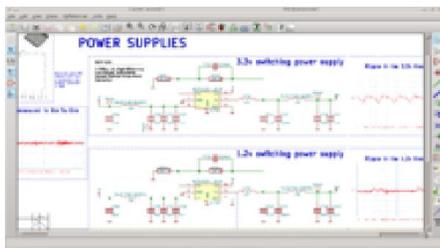
**Operating system** [Linux](#), [macOS](#), [Windows](#)

**Available in** 23 languages<sup>[5]</sup>

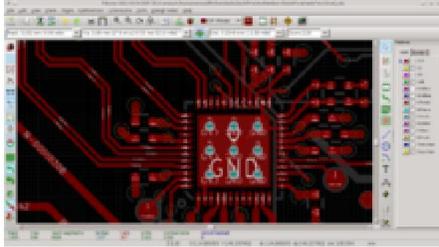
**Type** [EDA](#)

**License** [GNU GPL v3+](#)<sup>[6]</sup>

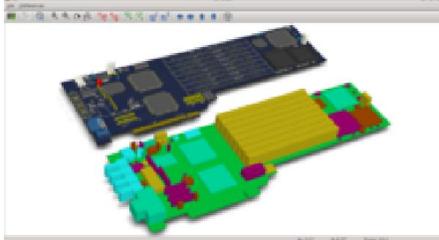
**Website** [www.kicad-pcb.org](http://www.kicad-pcb.org)



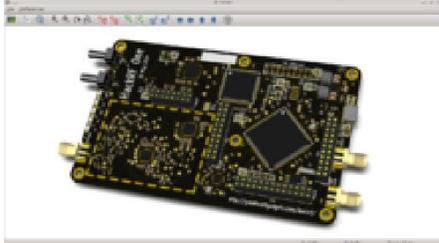
KiCad Eeschema for [schematic capture](#)



KiCad Pcbnew for layout design



KiCad 3D Viewer showing both VRML and [IDF](#) features on a demo board



KiCad 3D Viewer

**KiCad** (pronounced "Key-CAD"<sup>[7]</sup>) is a [free software](#) suite for [electronic design automation](#) (EDA). It facilitates the design of [schematics](#) for [electronic circuits](#) and their conversion to [PCB](#) designs. KiCad was originally developed by Jean-Pierre Charras. It features an integrated environment for [schematic capture](#) and PCB layout design. Tools exist within the package to create a [bill of materials](#), artwork, [Gerber](#) files, and 3D views of the PCB and its components.



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## History

KiCad was created in 1992 by Jean-Pierre Charras while working at [IUT de Grenoble](#).<sup>[8]</sup> Since then KiCad has gained a number of both volunteer and paid contributors. Notably in 2013 the [CERN](#) BE-CO-HT section started contributing resources towards KiCad to help foster open hardware development by helping improve KiCad to be on par with commercial EDA tools.

KiCad adopted a [point release](#) versioning scheme in December 2015 starting with KiCad 4.0.0. This was the first release featuring the more advanced tools implemented by CERN developers. CERN hopes to contribute further to the development of KiCad by hiring a developer through donations. Contributions may be made through the links on KiCad's website.

## Components

The KiCad suite has five main parts:

- KiCad – the project manager.
- Eeschema – the schematic capture editor.
- Pcbnew – the PCB layout program. It also has a 3D view.
- GerbView – the [Gerber](#) viewer.
- Bitmap2Component – tool to convert images to footprints for PCB artwork.

## Features

KiCad uses an integrated environment for all of the stages of the design process: Schematic capture, PCB layout, Gerber file generation/visualization, and library editing.

KiCad is a [cross-platform](#) program, written in C++ with [wxWidgets](#) to run on [FreeBSD](#), [Linux](#), [Microsoft Windows](#) and [Mac OS X](#). Many component libraries are available, and users can add custom components. The custom components can be available on a per-project basis or installed for use in any project. There are also tools to help with importing components from other EDA applications, for instance [EAGLE](#). There are also third party libraries available for KiCad, including [SnapEDA](#), and the [Digi-Key KiCad Library](#). Configuration files are in well documented plain text, which helps with interfacing [version control systems](#), as well as with automated component generation [scripts](#).

Multiple languages are supported, such as [Bulgarian](#), [Catalan](#), [Chinese](#), [Czech](#), [Dutch](#), [English](#), [Finnish](#), [French](#), [German](#), [Greek](#), [Hungarian](#), [Italian](#), [Japanese](#), [Korean](#), [Lithuanian](#), [Polish](#), [Portuguese](#), [Russian](#), [Slovak](#), [Slovene](#), [Spanish](#), and [Swedish](#).

## Eeschema

Eeschema has features including hierarchical schematic sheets, custom symbol creation, and an ERC (electrical rules check). Schematic symbols in Eeschema are very loosely coupled to footprints in Pcbnew to encourage reuse of footprints and symbols (e.g. a single 0805 footprint can be used for capacitors, resistors, inductors, etc.).

## Pcbnew

Internally Pcbnew supports up to 32 copper layers and 32 technical layers. Dimensions are stored with nanometer precision in signed 32-bit integers making the theoretical maximal PCB dimension  $2^{31}$  nm, or approximately 2.14 meters.

Currently<sup>[*citation needed*]</sup> Pcbnew is being heavily refactored, including getting a new rendering engine (called the graphics abstraction layer, or GAL) with [OpenGL](#) and [Cairo](#) back ends. Pcbnew is also getting a new tool framework to more easily allow developers to add tools without having to deal with supporting multiple renderers. Due to this, some tools are only available on the legacy [XOR-based renderer](#) and some are only available with the GAL renderers.

KiCad has a built-in [autorouter](#) for basic, single connections. Alternatively, Alfons Wirtz's open-source [Java](#)-based FreeRouting<sup>[9]</sup> can be used to externally autoroute boards. Anthony Blake's [Toporouter](#), a [topological autorouter](#) developed in 2008 for [gEDA PCB](#) as a Google-funded open source project mentored by [DJ Delorie](#),<sup>[10]</sup> has been adapted for use with KiCad as well.

A DRC (design rules check) is available to check for common logical errors.

The 3D PCB viewing function is based on [VRML](#) models, and the board model can be exported for [CAD](#) integration.

Some recent<sup>[*citation needed*]</sup> additions follow.

- An interactive router, which features the ability to walk around existing traces in the way or shove existing traces into a different position while maintaining their connectivity.
- High-speed PCB routing tools such as track-length matching and [differential pair](#) support.
- [Python](#) scripting support.

## Community

[Olimex Ltd](#), a provider of development tools and embedded device programmers, has announced that they have switched from [EAGLE](#) to KiCad as their primary EDA tool.<sup>[*citation needed*]</sup>

## See also

-  [Electronics portal](#)
-  [Free and open-source software portal](#)
- [Comparison of EDA software](#)
- [List of free and open source software packages](#)
- [List of free electronics circuit simulators](#)