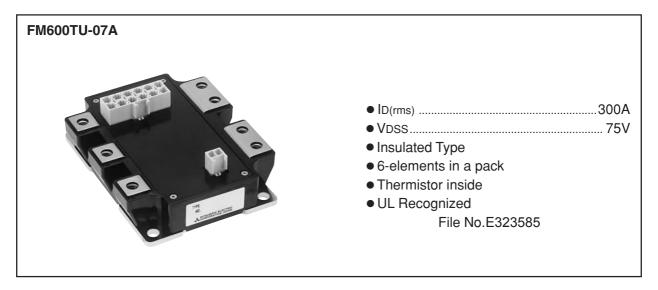
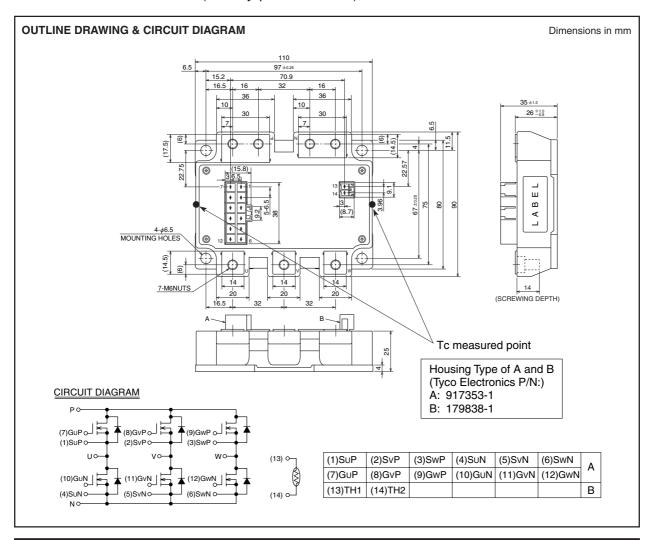
HIGH POWER SWITCHING USE INSULATED PACKAGE



#### **APPLICATION**

AC motor control of forklift (battery power source), UPS



### **HIGH POWER SWITCHING USE INSULATED PACKAGE**

#### ABSOLUTE MAXIMUM RATINGS (Tj = 25°C unless otherwise specified.)

Symbol	Item	Conditions	Rating	Unit
VDSS	Drain-source voltage	G-S Short	75	V
Vgss	Gate-source voltage	D-S Short	±20	V
ID	Drain current	$Tc' = 139^{\circ}C^{*3}$	300	Α
IDM	Drain current	Pulse*2	600	Α
IDA	Avalanche current	$L = 10\mu H \text{ Pulse}^{*2}$	300	Α
Is*1	Source current		300	Α
Ism*1	Source current	Pulse*2	600	Α
Po*4	Maximum namer dissinction	Tc = 25°C	960	W
Po*4	Maximum power dissipation	$Tc' = 25^{\circ}C^{*3}$	1300	W
Tch	Channel temperature		-40 ~ +150	°C
Tstg	Storage temperature		<b>−</b> 40 ~ +125	°C
Visol	Isolation voltage	Main terminal to base plate, AC 1 min, f=60Hz, RMS	2500	V
_	Mounting torque	Main Terminal M6	3.5 ~ 4.5	N∙m
		Mounting to heat sink M6	3.5 ~ 4.5	N∙m
_	Weight	Typical value	600	g

## **ELECTRICAL CHARACTERISTICS** (Tj = $25^{\circ}$ C unless otherwise specified.)

Cumahad	Item	Conditions		Limits			Unit
Symbol				Min.	Тур.	Max.	Unit
IDSS	Drain cutoff current	VDS = VDSS, VGS = 0V		_	_	1	mA
VGS(th)	Gate-source threshold voltage	ID = 30mA, VDS = 10V		4.7	6	7.3	V
Igss	Gate leakage current	VGS = VGSS, VDS = 0V		_	_	1.5	μΑ
rDS(on)	Static drain-source	ID = 300A Tj = 25°C		_	0.53	0.73	
(chip)	On-state resistance	VGS = 15V	Tj = 125°C	_	0.87	_	mΩ
VDS(on)	Static drain-source	ID = 300A	Tj = 25°C	_	0.16	0.22	V
(chip)	On-state voltage	VGS = 15V	Tj = 125°C	_	0.26	_	
RDD'-SS'	Internal lead resistance	ID = 300A	Tj = 25°C	_	0.7	_	mΩ
		terminal-chip	Tj = 125°C	_	1.0	_	
Ciss	Input capacitance	VDS = 10V VGS = 0V VDD = 48V, ID = 300A, VGS = 15V		_	_	110	nF
Coss	Output capacitance			_	_	15	
Crss	Reverse transfer capacitance			_	_	10	
QG	Total gate charge			_	1650	_	nC
td(on)	Turn-on delay time	$VDD = 48V, \ ID = 300A, \ VGS1 = VGS2 = 15V$ $RG = 4.2\Omega, \ Inductive \ load \ switching \ operation$ $IS = 300A$		_	_	450	ns
tr	Rise time			_	_	600	
td(off)	Turn-off delay time			_	_	600	
tf	Fall time			_	_	600	
trr*1	Reverse recovery time			_	_	200	ns
Qrr*1	Reverse recovery charge			_	4.8	_	μС
Vsp*1	Source-drain voltage	Is = 300A, VGS = 0V		_	_	1.3	V
Rth(j-c)	The survey of the same of	MOSFET part (1/6 module)*7 MOSFET part (1/6 module)*3		_	_	0.13	K/W
Rth(j-c')	Thermal resistance			_	_	0.096	
Rth(c-s)	0	Case to fin, Thermal grease Applied*8 (1/6 module)		_	0.1	_	
Rth(c'-s')	Contact thermal resistance  Case to fin, Thermal grease Applied *3, *8 (1/6 module)		_	0.09	_	1	

#### NTC THERMISTOR PART

Symbol	Parameter	Conditions	Limits			Llmit
			Min.	Тур.	Max.	Unit
R25*6	Resistance	$TTH = 25^{\circ}C^{*5}$	_	100	_	kΩ
B*6	B Constant	Resistance at TTH = 25°C, 50°C*5	_	4000	_	K

<sup>\*7:</sup> To measured point is shown in page OUTLINE DRAWING. \*8: Typical value is measured by using thermally conductive grease of  $\lambda$ =0.9 W/(m·K).



<sup>\*1:</sup> It is characteristics of the anti-parallel, source to drain free-wheel diode (FWDi).
\*2: Pulse width and repetition rate should be such that the device junction temperature (Tj) does not exceed Tj max rating.

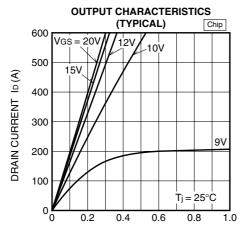
<sup>\*3:</sup> Tc' measured point is just under the chips. If use this value, Rth(s-a) should be measured just under the chips. \*4: Pulse width and repetition rate should be such as to cause negligible temperature rise.

<sup>\*5:</sup> TTH is thermistor temperature.

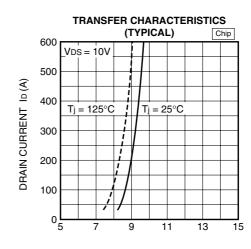
<sup>\*6:</sup> B = (InR1-InR2)/(1/T1-1/T2) R1: Resistance at T1(K), R2: Resistance at T2(K)

HIGH POWER SWITCHING USE **INSULATED PACKAGE** 

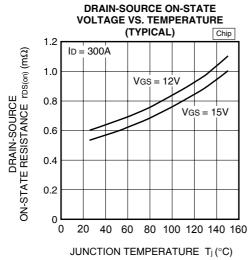
#### **PERFORMANCE CURVES**

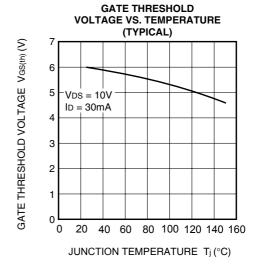


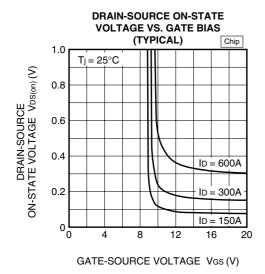
DRAIN-SOURCE VOLTAGE VDS (V)



GATE-SOURCE VOLTAGE Vgs (V)





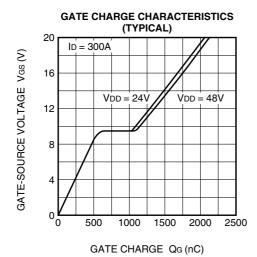


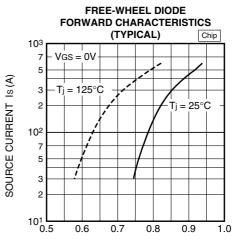
CAPACITANCE VS. **DRAIN-SOURCE VOLTAGE** (TYPICAL) 102 5 3 (nF 2 CAPACITANCE 10<sup>1</sup> 5 VGS = 0V10<sup>0</sup> 10<sup>-1</sup> 2 3 5 7 10<sup>0</sup> 2 3 5 7 10<sup>1</sup> 2 3

DRAIN-SOURCE VOLTAGE VDS (V)

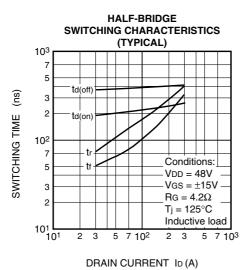


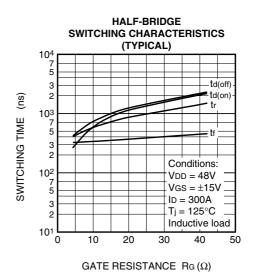
# HIGH POWER SWITCHING USE INSULATED PACKAGE

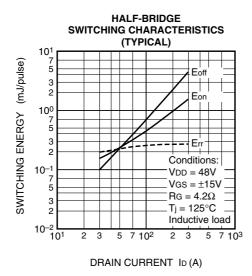


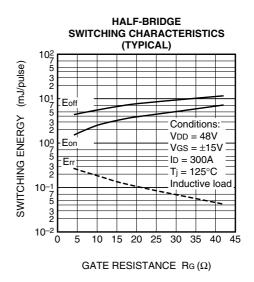


SOURCE-DRAIN VOLTAGE VSD (V)



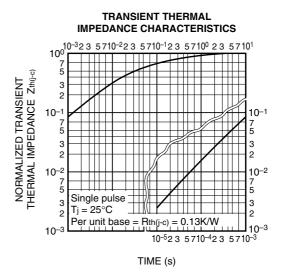




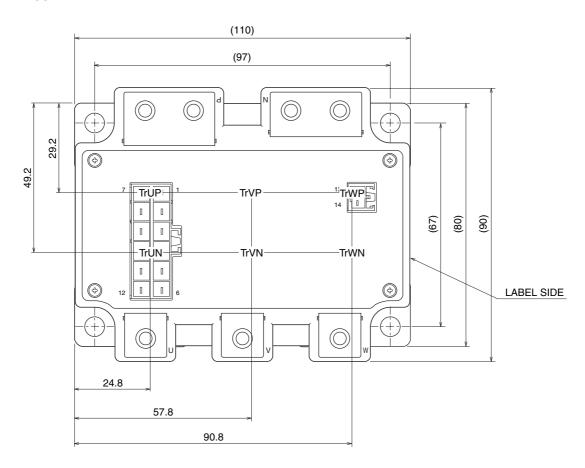


HIGH POWER SWITCHING USE INSULATED PACKAGE

#### **REVERSE RECOVERY CHARACTERISTICS** OF FREE-WHEEL DIODE (TYPICAL) 10<sup>3</sup> 5 3 trr 10<sup>2</sup> Irr (A), trr (ns) 5 3 Conditions: 10<u>1</u> VDD = 48V $VGS = \pm 15V$ 5 $R\mathsf{G}=4.2\Omega$ 3 $T_j = 25^{\circ}C$ Inductive load 100 L 2 3 5 7 102 2 5 7 10<sup>3</sup> SOURCE CURRENT Is (A)



#### **CHIP LAYOUT**



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