



Employment Opportunities at Pericom

Pericom Semiconductor Corporation, ranked number four in [Silicon Valley's "Fast 50"](#), is a leading edge company that designs and markets high-performance logic and mixed-signal integrated circuits for the computing and data communication markets. Our fast growth has led to the creation of many opportunities for well qualified professionals to join our team...

San Jose Headquarters

- [Director of Applications](#)
- [Design Manager/Director](#)
- [Process and Device Development Engineer](#)
- [Device Characterization & Modeling Engineer](#)
- [Senior/Staff Mixed Signal Design Engineer](#)
- [Product Marketing Engineer-Clock Products](#)
- [Product Marketing Engineer-Datcom Products](#)
- [Tactical Marketing Engineer](#)
- [Pacific Rim Sales Manager](#)
- [Field /Application Engineers](#)
- [Design Engineers](#)
- Marketing Administrative Assistant
- Executive Secretary

Shanghai, China

- [Design Engineering Manager - Pericom Shanghai](#)
- [Design Engineering - Pericom Shanghai](#)

Director of Applications

- Develop and manage an organization responsible for new product definition and applications support.

- Good understanding of high-performance computer and networking system requirements and development trends.
- Strong technical knowledge in the applications of high-speed digital and mixed-signal integrated circuits
- Technical degree with 10+ years experience.
- Strong management skill & works well with customers.

Design Manager/Director

- Lead an experienced design team to develop high performance datacom transceiver products
- Familiar with LAN/WAN standards & product development
- Experienced in mixed signal and complex ASIC design
- MSEE/PhD with 10+ years design experience and strong management skills.

Process and Device Development Engineer

- Work with our foundry partners to develop new processes as well as resolve process and device issues.
- Characterize new processes and devices.
- Perform yield analysis and assist in development of on-chip ESD protection structures.
- MSEE/PhD (preferred) with 1-3 years of industry experience. Knowledge of device physics and interactions among processes.

Device Characterization & Modeling Engineer

- Responsible for device measurement, characterization, and modeling & extracting SPICE parameters and parasites.
- Work with design CAD group to improve circuit simulation accuracy.
- Must understand MOS circuits and device physics. MOSFET noise modeling for RF design desirable.
- MSEE/PhD preferred with 1-3 years related industry experience.

Senior/Staff Mixed Signal Design Engineer

- Design high-performance datacom transceivers.
- Experienced in high-performance mixed-signal/analog design.
- Previous experience in LAN preferred.
- MSEE with minimum of 7+ years experience.

Product Marketing Engineer-Clock Products

- Responsible for product strategy and management of the fast growth clock product line.
- Lead product definition for high-performance PLL-based clock products for PC, PC peripherals & Datacom markets.
- Launch new products with coordinated marcom plan and drive reference design wins with our key customers.

- BSEE/Technical degree required with 2-5 years industry experience.

Product Marketing Engineer-Datcom Products

- Responsible for product strategy and management of one or more of the following product families: LAN, WAN, PCI.
- Identify new market opportunities and lead product definition in collaboration with customers and applications engineering.
- Launch new products with coordinated marcom plan and drive reference design wins with our key customers.
- BSEE/Technical degree required with 2-5 years industry experience.

Tactical Marketing Engineer

- Provide tactical marketing support for logic, clock & mixed-signal products with responsibility for price management, business analysis, sales training, and collateral.
- Drive regional business plans and account development in collaboration with the sales organization.
- Develop & maintain production forecast for manufacturing
- Technical or Business degree required with 3-5 years industry experience or equivalent. Strong analytical and computer skills.

Pacific Rim Sales Manager

- Responsible for management of sales representatives and distributors as well as penetrating major OEM accounts in Japan and Korea.
- Requires 3-5 years sales/sales management experience & a proven track record selling semiconductors to major OEMs.
- Prefer background in a company with a strong market presence in PCs and networking.
- Must be familiar with Japanese and Korean markets.
- Technical degree, BSEE or MSEE preferred. Japanese language skills a plus. Potential exists to work out of Hong Kong office or establish office in Japan.

Field /Application Engineers

- Responsible for designing in products at OEM and distributor accounts.
- Train field sales personnel, factory direct, reps, and distributors. Work with marketing to identify new product opportunities.
- Requires BSEE/MSEE, preferred. One to three years design experience helpful, but not required. One to three years of field applications helpful, but not required.
- Opportunities exist in Western U.S., working out of Northern or Southern California, or in Eastern U.S., working out of a Boston, Atlanta, or Raleigh office.

Design Engineers

- Design high-performance logic products

- Familiar with high-speed, low noise I/O design
- Experience with ASIC design methodology a strong plus
- MS/Ph.D. degree in EE
- Opportunity to work on high-speed logic/mixed signal design
- Familiar with HDL/Verilog, Synopsys desirable
- *Senior staff design engineer positions require a minimum of BSEE with advanced degree preferred. Also, require 5 years + design experience and working knowledge of advanced CAD design tools, preferably Cadence platform.*

PERICOM-SHANGHAI VENTURE

Design Engineering Manager

- Responsible to lead our design engineering team in Shanghai
- Requires strong VLSI design and engineering management experience
- Familiarity with Verilog Synopsys design tools preferred
- Experience in CMOS high-performance mixed signal design a strong plus
- Frequent travel to China required

Design Engineering

- Requires hands on experience in VLSI ASIC design
- Familiar with state of art design tools - preferably Cadence platform
- CMOS mixed signal design experience a strong plus
- Frequent travel to China required
- *Positions require a minimum BSEE with advanced degree preferred. Five plus years of technical experience required.*

Pericom offers a technologically challenging environment, competitive salary, stock options, stock purchase plan, 401(K) and medical/dental benefits. Send resumes to Pericom, 2380 Bering Drive, San Jose, CA 95131 or Fax to 408 435-1100, Attn: Personnel. No phone calls please.

Pericom is an Equal Opportunity Employer. For more information, see our Website: www.pericom.com.

To contact Pericom:

1. Fill out our [online resume form](#) for immediate review!
2. Or mail your resume to:

Pericom Semiconductor Corporation

2380 Bering Drive
San Jose, CA 95131
Attn: Personnel.

3. Or fax it to (408) 435-1100
Attn: Personnel.

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